

Pitch 1.0mm PCB Impedance/Insertion Loss Test Probe (PRB-100-P10-2BM) Datasheet

Version 1.0

◆Introduction:

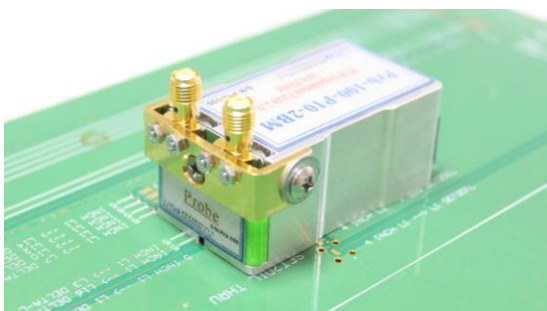


Fig1: PCB Impedance measurement with pitch 1.0mm test probe.

Impedance and Loss measurements on PCB become more and more important when the data rates increased. To get the pure loss of the PCB traces based on the assigned length, especially in the inner layer is very difficult because How to remove the via hole and probe effects is an issue. However, if the probe itself can't be maintained with a linear behavior, anything is invalid to discuss.

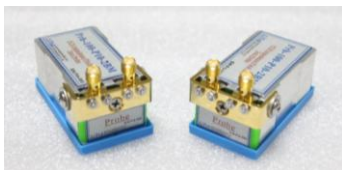
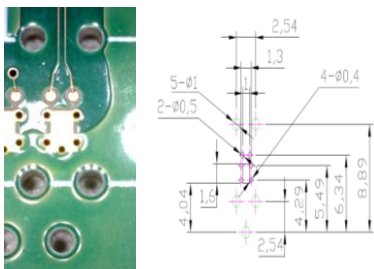
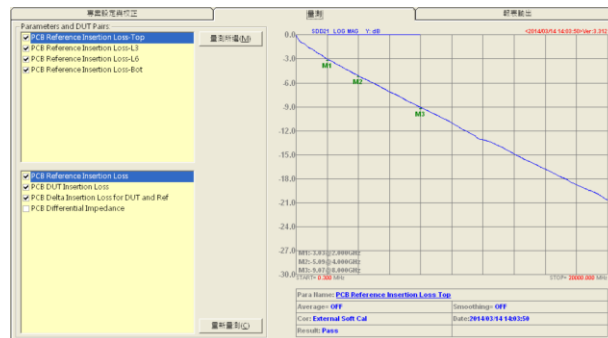


Fig2: Probe on the holder

Litek's PCB test probe [PRB-100-P10-2BM](#) is a selection for the user to measure the PCB Impedance and Loss. We defined the guide pin and the contact pads for the user to design the PCB traces. A preferred assistant structure is defined that the probe can be easily putted on the right position.



LiTek's TpNA for PCB Measurement Software can be used to help the user finish all steps of PCB Impedance and Insertion Loss measurement.



Agilent E5071C

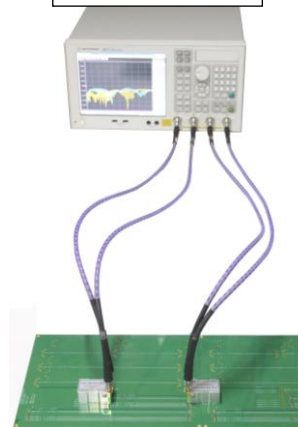


Fig3: Measurement result of Impedance and Insertion Loss up to 20GHz.

◆Features:

1. Good Impedance match: $100\Omega \pm 3\Omega$.
2. Low Intra Pair Skew: $\pm 2ps$.
3. Fast Rise Time: Less than 50 ps.
4. Low Loss design: 1.5 dB @20GHz reference.
5. 2 SMA or 3.5mm connectors.
6. Option: TRL Kit is available to remove the effect of Probe

Contact LiTek:

4F-3, No. 18, Huannan Rd., Pingzhen City, Taoyuan County
32448, Taiwan

Tel: +886-3-4951851 Fax: +886-3-4951841

李文福 經理 Leo Lee leo@litetw.com

Cell Phone: +886-922330325